

AMENDMENTS TO THE FIGURES

Included as an appendix is replacement Figure 17 and an annotated copy of Figure 17 showing the change made. In Figure 17, the bit labeled "F3" is relabeled "F2."

Attachments: Replacement Figure 17
Annotated Figure 17 Showing Changes

REMARKS

Claims 1-12 and 14-29 are pending. Claims 1, 10 and 26 are amended herein. No new matter is added as a result of the claim amendments.

103 Rejections

The instant Office Action states that Claims 1-12 and 14-29 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Myers et al. ("Myers;" U.S. Patent No. 6,185,127) in view of Gorecki et al. ("Gorecki;" U.S. Patent No. 6,701,340).

Applicants respectfully agree with the statement in the instant Office Action with regard to independent Claims 1, 10 and 26 that Myers does not teach electrically coupling selected analog blocks according to configuration information in the configuration register.

Applicants respectfully submit that Gorecki does not overcome the shortcomings of Myers. Gorecki describes the use of an analog routing pool that receives signals and routes them to the analog blocks. According to Gorecki, the analog blocks only communicate with each other via the analog routing pool, depending on the programming of the analog routing pool. However, according to the present claimed invention, the analog blocks are configured to route signals from one block to another.

Specifically, Applicants respectfully submit that Gorecki, alone or in combination with Myers, does not show or suggest a method that includes "electrically coupling selected analog blocks according to said configuration

information to achieve said analog function, wherein said configuration information is implemented by said analog blocks to accomplish said coupling” as recited in independent Claim 1 (emphasis added). Claims 2-9 are dependent on Claim 1 and recite additional limitations.

Applicants also respectfully submit that Gorecki, alone or in combination with Myers, does not show or suggest a method that includes “programming said first analog block according to said configuration information to route signals from said first analog block to said second analog block” as recited in independent Claim 10. Claims 11-12 and 14-25 are dependent on Claim 10 and recite additional limitations.

Also, Applicants also respectfully submit that Gorecki, alone or in combination with Myers, does not show or suggest a method that includes “receiving configuration information into a configuration register of said device, wherein said configuration information is for causing a first analog block of said device to be selectively and electrically coupled to a second analog block of said device according to an analog function to be performed, said configuration information for configuring said first analog block to route a signal to said second analog block” as recited in independent Claim 26 (emphasis added). Claims 27-29 are dependent on Claim 26 and recite additional limitations.

Therefore, Applicants respectfully submit that Myers and Gorecki, alone or in combination, do not show or suggest the present claimed invention as recited in independent Claims 1, 10 and 26. Accordingly,

Applicants respectfully submit that independent Claims 1, 10 and 26 traverse the basis for rejection under 35 U.S.C. § 103(a) and are in condition for allowance. As such, Applicants also respectfully submit that Claims 2-9, 11-12, 14-25 and 27-29 traverse the basis for rejection under 35 U.S.C. § 103(a), as these claims are dependent on allowable base claims and recite additional limitations.

Conclusions

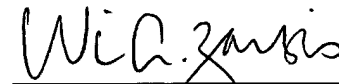
In light of the above remarks, reconsideration of the rejected claims is respectfully requested. Based on the arguments presented above, it is respectfully asserted that Claims 1-12 and 14-29 overcome the rejections of record and, therefore, allowance of these claims is solicited.

The Examiner is invited to contact Applicants' undersigned representative if the Examiner believes such action would expedite resolution of the present Application.

Date: 11/1/04

Respectfully submitted,

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Attachments: Replacement Figure 17
Annotated Figure 17 Showing Changes



Application No. 09/930,021
Reply to Office Action of 8/31/04
Annotated Sheet Showing change

151 → • Word 1 (LSB) F0 F1 **F2** G N/C N/C N/C N/C (MSB)
152 → • Word 2 (LSB) P0 P1 P2 N0 N1 N2 CEN OS (MSB)
153 → • Word 3 (LSB) S0 S1 N/C N/C R0 R1 R2 R3 (MSB)

"F3" replaced with "F2"

Figure 17

- Word 0 (LSB) A0 A1 A2 A3 A4 S PS OSZ (MSB)
- Word 1 (LSB) B0 B1 B2 B3 B4 I11 I12 IS3 (MSB)
- Word 2 (LSB) C0 C1 C2 C3 C4 AZ CS OS (MSB)
- Word 3 (LSB) S0 S1 IS21 IS22 DO1 DO2 R1 R2 (MSB)

Figure 18A

- Word 0 (LSB) A0 A1 A2 A3 A4 S PS OSZ (MSB)
- Word 1 (LSB) B0 B1 B2 B3 B4 I11 I12 I13 (MSB)
- Word 2 (LSB) C0 C1 C2 C3 C4 AZ CS OS (MSB)
- Word 3 (LSB) S0 S1 MB N/C DO1 DO2 R1 R2 (MSB)

Figure 18B